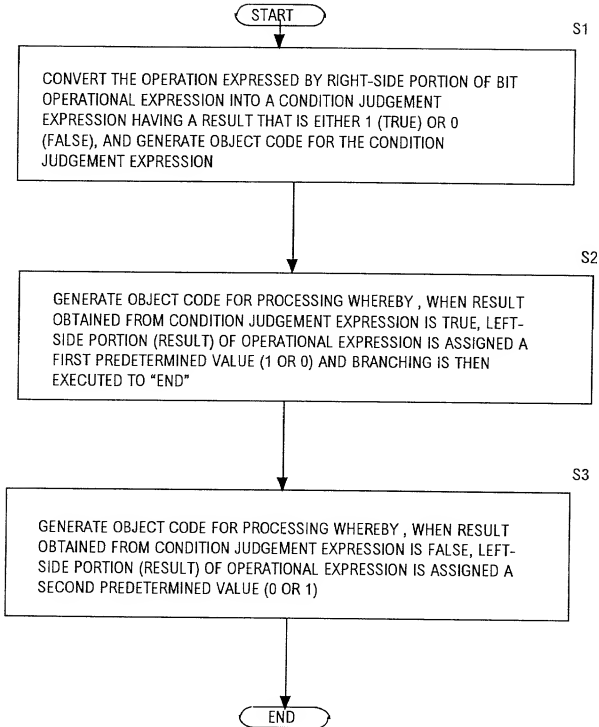


FIG. 1



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FIG. 2

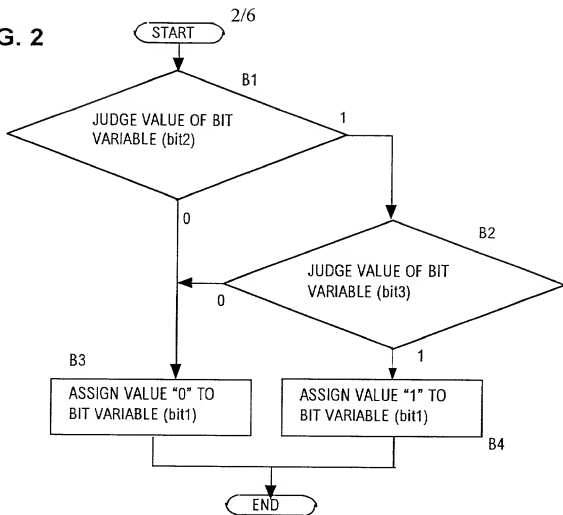


FIG. 3

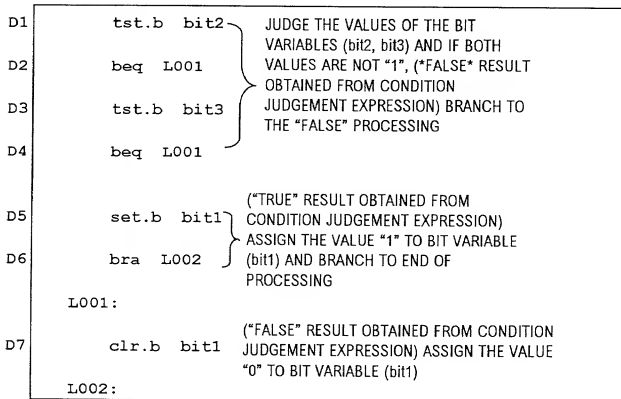


FIG. 4

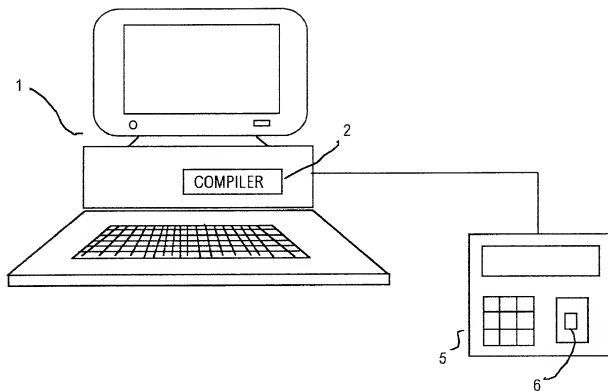


FIG. 5

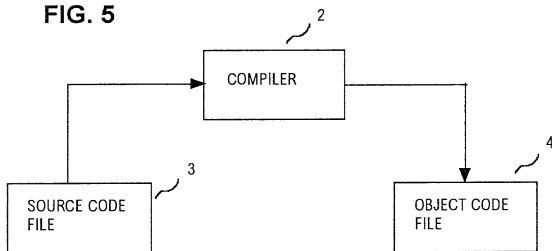


FIG. 6

MICROCOMPUTER 6

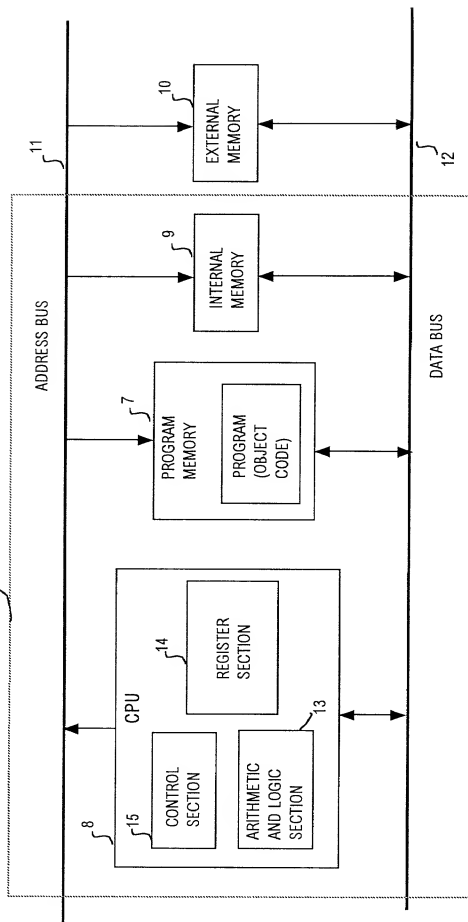


FIG. 7

```

mov  BIT2, reg1
shr1 reg1, reg1
shr1 reg1, reg1
shr1 reg1, reg1
and  #$0001, reg1

mov  BIT3, reg2
shr1 reg2, reg2
shr1 reg2, reg2
shr1 reg2, reg2
and  #$0001, reg2

and  reg2, reg1 .....

shl1 reg1, reg1
shl1 reg1, reg1
shl1 reg1, reg1
mov  BIT1, reg2
and  #$fff7, reg2
or   reg1, reg2
mov  reg2, BIT1

```

C1: READ OUT A BIT VARIABLE (bit2) AND LOAD INTO A GENERAL-PURPOSE REGISTER (reg1)

C2: READ OUT A BIT VARIABLE (bit3) AND LOAD INTO A GENERAL-PURPOSE REGISTER (reg2)

C3: DERIVE THE LOGIC PRODUCT OF THE VALUES OF bit2 AND bit3

C4: ASSIGN THE RESULT OF THE LOGIC PRODUCT (FROM reg1) AS THE VALUE OF A BIT VARIABLE (bit1)

FIG. 8

PRIOR ART

